

# SILVER OAK COLLEGE OF ENGINEERING & TECHNOLOGY

## EC DEPARTMENT

### TIME TABLE FOR VIVA/PRACTICAL EXAMINATION (Winter 2018)

Date: 10/10/2018

<b>Date/ Time</b>	<b>3<sup>rd</sup> SEM</b>
<b>12/10/2018</b>	<b>2130901 CIRCUITS AND NETWORKS</b>
<b>13/10/2018</b>	<b>2131004 DIGITAL ELECTRONICS</b>
<b>15/10/2018</b>	<b>2131006 ELECTRONICS DEVICES &amp; CIRCUITS</b>
<b>16/10/2018</b>	<b>2131005 ELECTRICAL MACHINES</b>
<b>17/10/2018</b>	<b>2130002 ADVANCED ENGINEERING MATHEMATICS</b>
<b>19/10/2018</b>	<b>2130004 ENGINEERING ECONOMICS &amp; MANAGEMENT</b>

**Venue: Respective Lab Locations**

**Prof. Vimal H. Nayak  
(H.O.D – EC)**

# SILVER OAK COLLEGE OF ENGINEERING & TECHNOLOGY

## EC DEPARTMENT

### TIME TABLE FOR VIVA/SUBMISSION EXAMINATION (Winter 2018)

Date: 10/10/2018

<b>Date/ Time</b>	<b>5<sup>th</sup> SEM</b>
<b>12/10/2018</b>	<b>2151001 Microcontroller and interfacing (M&amp;I)</b>
<b>13/10/2018</b>	<b>2151002 Engineering electromagnetics (EMT)</b>
<b>15/10/2018</b>	<b>2131004 Electronic Communication (EC)</b>
<b>16/10/2018</b>	<b>2151101 Audio video systems (AVS)</b>
<b>17/10/2018</b>	<b>21510001 Design engineering II A (DEIIA)</b>
<b>19/10/2018</b>	<b>2151102 Mini project /Cyber Security</b>

**Venue: Respective Lab Locations**

**Prof. Vimal H. Nayak**

**EC DEPARTMENT**

**TIME TABLE FOR VIVA/ SUBMISSION EXAMINATION (Winter 2018)**

**Date: 10/10/2018**

<b>Date/ Time</b>	<b>7<sup>th</sup> SEM</b>
<b>12/10/2018</b>	<b>2171003 Digital Signal Processing</b>
<b>13/10/2018</b>	<b>2171004 Wireless Communication</b>
<b>15/10/2018</b>	<b>2171008 Data Communication &amp; Networking</b>
<b>16/10/2018</b>	<b>2171001 Microwave Engineering</b>
<b>17/10/2018</b>	<b>2171007 Satellite Communication</b>
<b>19/10/2018</b>	<b>2170001 Project</b>

**Venue: B-208**

**Prof. Vimal H. Nayak**